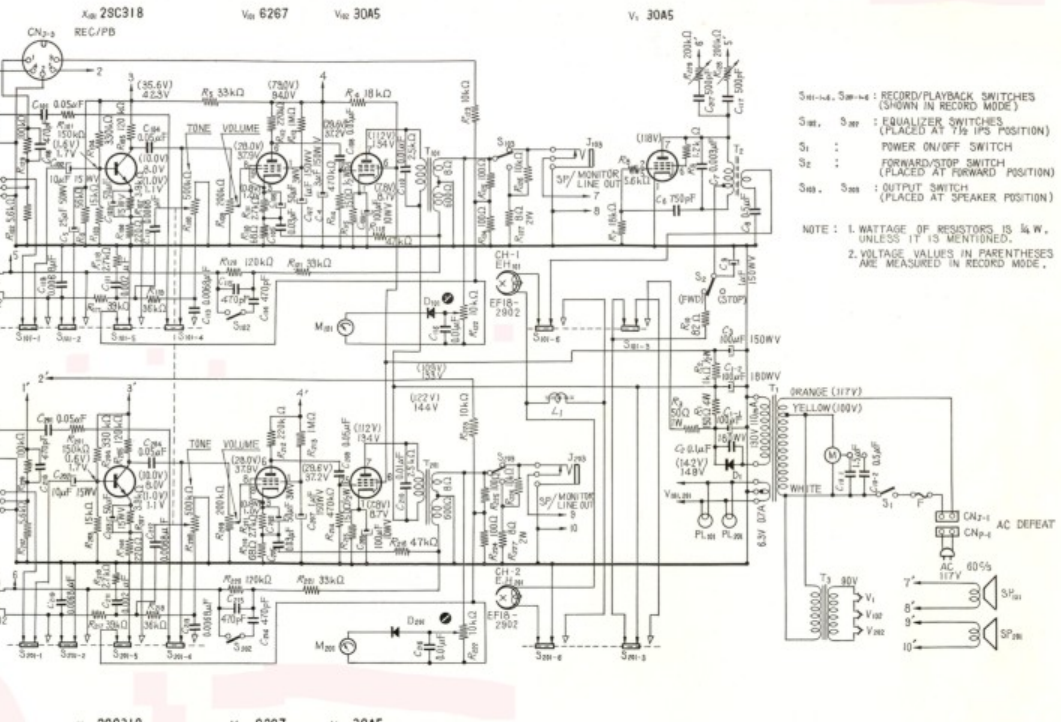
Schematic Diagram



$S_{101-1,4}$, $S_{201-1,4}$: RECORD/PLAYBACK SWITCHES (SHOWN IN RECORD MODE)

S_{102}, S_{202} : EQUALIZER SWITCHES (PLACED AT 7½ IPS POSITION)

S_1 : POWER ON/OFF SWITCH

S_2 : FORWARD/STOP SWITCH (PLACED AT FORWARD POSITION)

S_{103}, S_{203} : OUTPUT SWITCH (PLACED AT SPEAKER POSITION)

NOTE : 1. WATTAGE OF RESISTORS IS ¼ W, UNLESS IT IS MENTIONED.

2. VOLTAGE VALUES IN PARENTHESES ARE MEASURED IN RECORD MODE.